Notice of Allowability	Application No.	Applicant(s)
	10/518,614	PAPPALARDO ET AL.
	Examiner	Art Unit
	Robert E. Carter	2609
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	plication. If not included
1. This communication is responsive to 12/23/04.		
2. ☑ The allowed claim(s) is/are <u>1-8</u> .		
<ul> <li>3.  Acknowledgment is made of a claim for foreign priority un</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> <li>3.  Copies of the certified copies of the priority documents</li> </ul>	been received. been received in Application No	<del></del>
International Bureau (PCT Rule 17.2(a)).	Minorito 11440 00011 10001104 11 4110 1	national stage application from the
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONMI THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply of ENT of this application.	complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give:	tted. Note the attached EXAMINER's reason(s) why the oath or declarate	S AMENDMENT or NOTICE OF tion is deficient.
5. CORRECTED DRAWINGS ( as "replacement sheets") must	t be submitted.	
(a) including changes required by the Notice of Draftsperson		948) attached
1) hereto or 2) to Paper No./Mail Date		
(b) including changes required by the attached Examiner's Paper No./Mail Date		,
Identifying indicia such as the application number (see 37 CFR 1.8 each sheet. Replacement sheet(s) should be labeled as such in the	84(c)) should be written on the drawin he header according to 37 CFR 1.121(d	igs in the front (not the back) of I).
<ol> <li>DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT F</li> </ol>	it of BIOLOGICAL MATERIAL m FOR THE DEPOSIT OF BIOLOGICA	nust be submitted. Note the AL MATERIAL.
		-
Attachment(s)	_	
I. Notice of References Cited (PTO-892)	5. Notice of Informal Pa	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary ( Paper No./Mail Date	
B. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 12/23/2004	7. 🛭 Examiner's Amendm	pent/Comment
I. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	8.   Examiner's Statemer	nt of Reasons for Allowance
	9.	
	CHRI	Xlly S KELLEY

U.S. Patent and Trademark Office PTOL-37 (Rev. 08-06)

**Notice of Allowability** 

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600 / Mail Date 200704274

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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Carol W. Burton on 4/27/07.

The application has been amended as follows:

In claim 1, line 9, change "[[(VSS) supply voltage,]];" to -- [[(VSS)]] supply voltage; --.

## Allowable Subject Matter

1. The following is a statement of reasons for the indication of allowable subject matter:

Kanda et al. (US patent # 5,731,795), the closest prior art, discloses a column driver for a liquid crystal display with 4 voltage levels (Fig. 6, VCC, VPC, VDC, ground) connected to two pair of series transistors (Fig. 6, #38P1 & 38N1, 39P2 & 39 N2) connected to turnoff circuitry (Fig. 6, #33) which alternately deactivates one pair of transistors while one transistor of the other pair is actuated (Abstract, lines 1-9), and a pair of elevator devices (Fig. 6, #34, 35), connected to one transistor (Fig. 6, #38N1), and controlled by logic circuitry (Fig. 6, #32).

Kanda et al. therefore does not teach the limitations in claim 1 of the instant application requiring the turnoff circuitry to be coupled to the elevator devices, and further requiring the elevator devices to be coupled to one of said pairs of transistors.

While Kanda et al. does disclose the concept of alternately deactivating one pair of transistors while one transistor of the other pair is actuated, the differences in structure between Kanda et al. and the instant application are significant and non-obvious.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Robert E. Carter whose telephone number is 571-270-3006. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chris Kelley can be reached on 571-272-7331. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

**REC** 

CHRIS KELLEY
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